

J-Link cannot find IC

Questions:

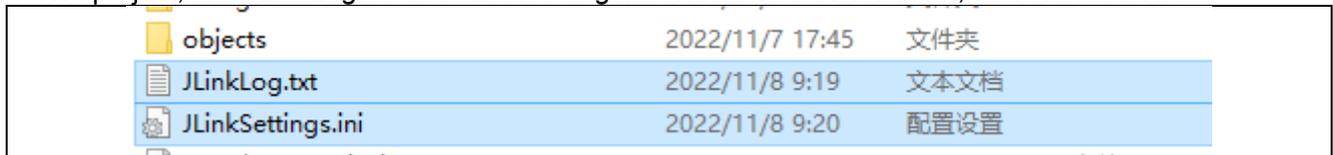
Jlink cannot find IC in Keil or IAR project (related to compilation environment)

Answer:

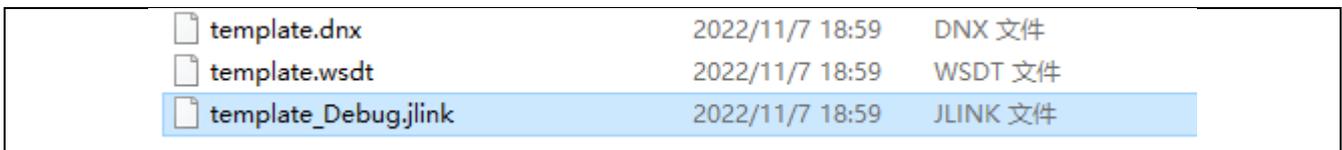
When the Keil project compiled by the engineer was provided to other engineers, it is found that the J-Link cannot find IC even if the IC can be found by ICP software and the project can be compiled successfully. In most cases, the possible reason is that Keil or IAR compiler calls its built-in Segger driver that does not support AT32 MCU.

Method 1:

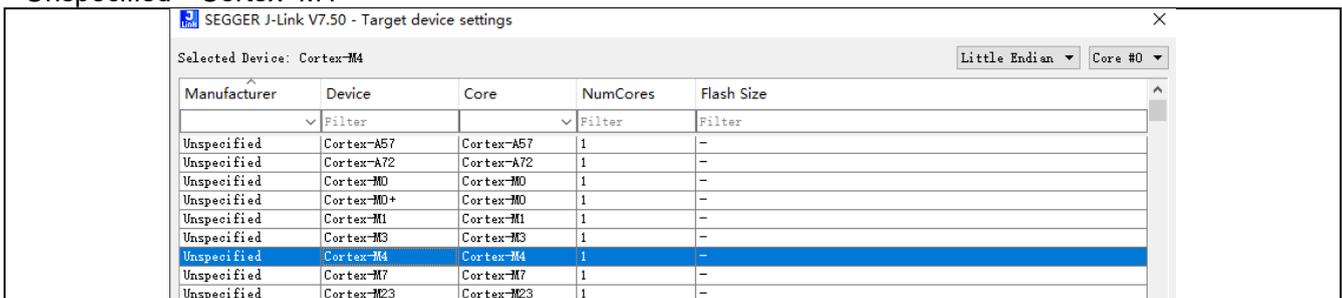
1) In Keil project, find *JLinkLog.txt* and *JLinkSettings.ini* folders and delete them, as shown below:



In IAR project, delete *xxx.jlink* in the *settings* folder

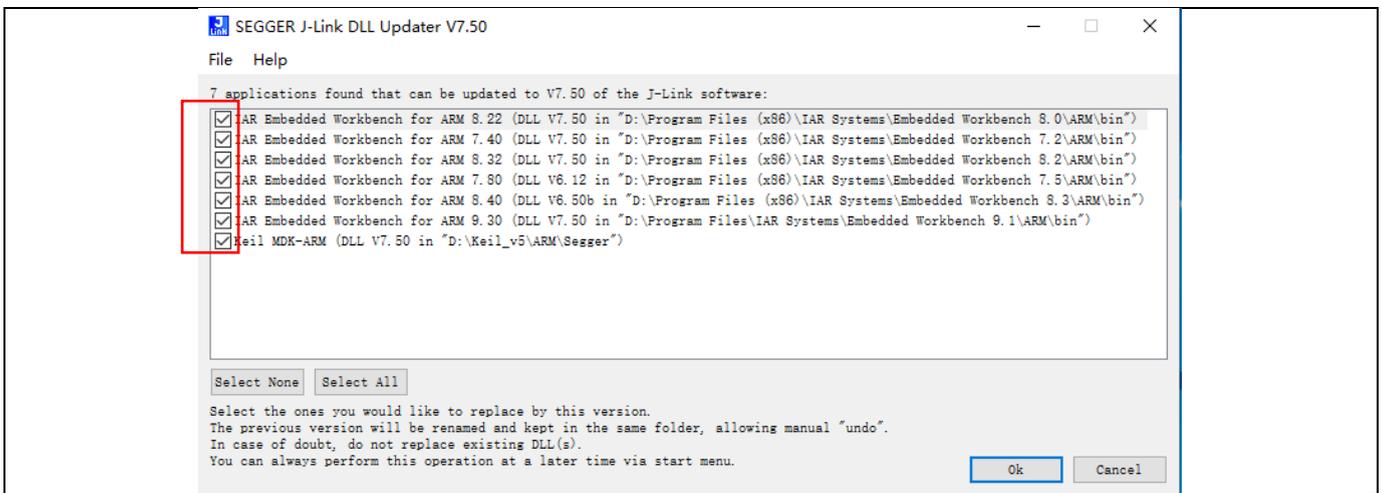
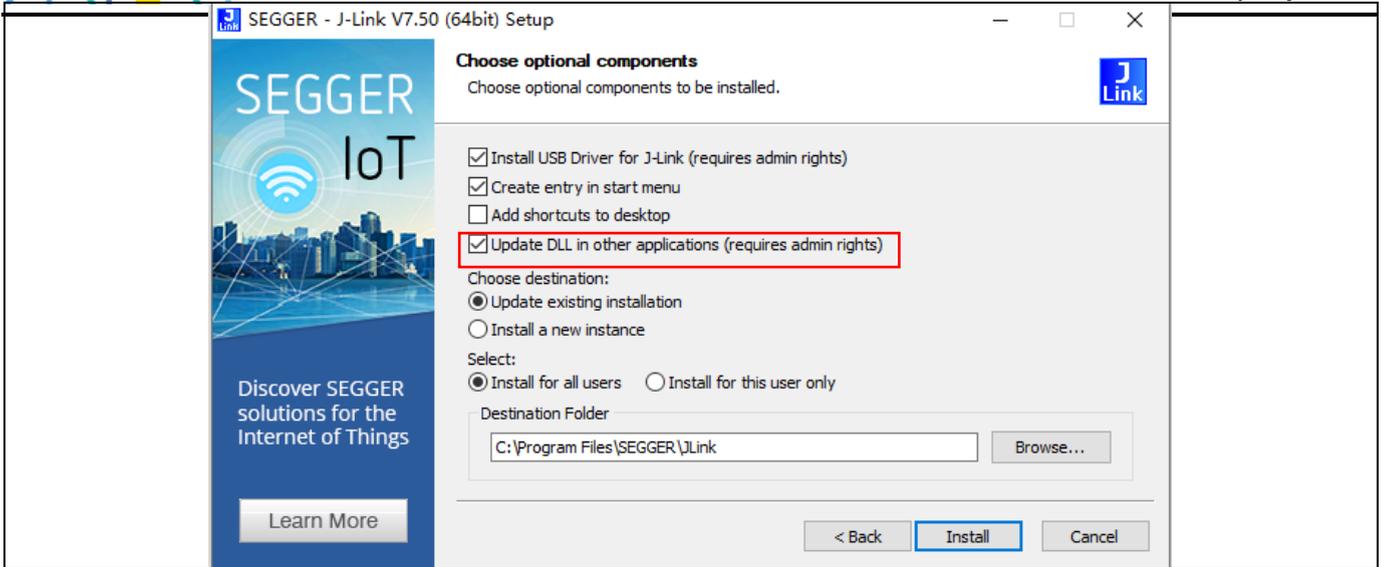


2) From the drop-down box of Segger J-ink, select a core type corresponding to MCU you are using, such as "Unspecified Cortex-M4"

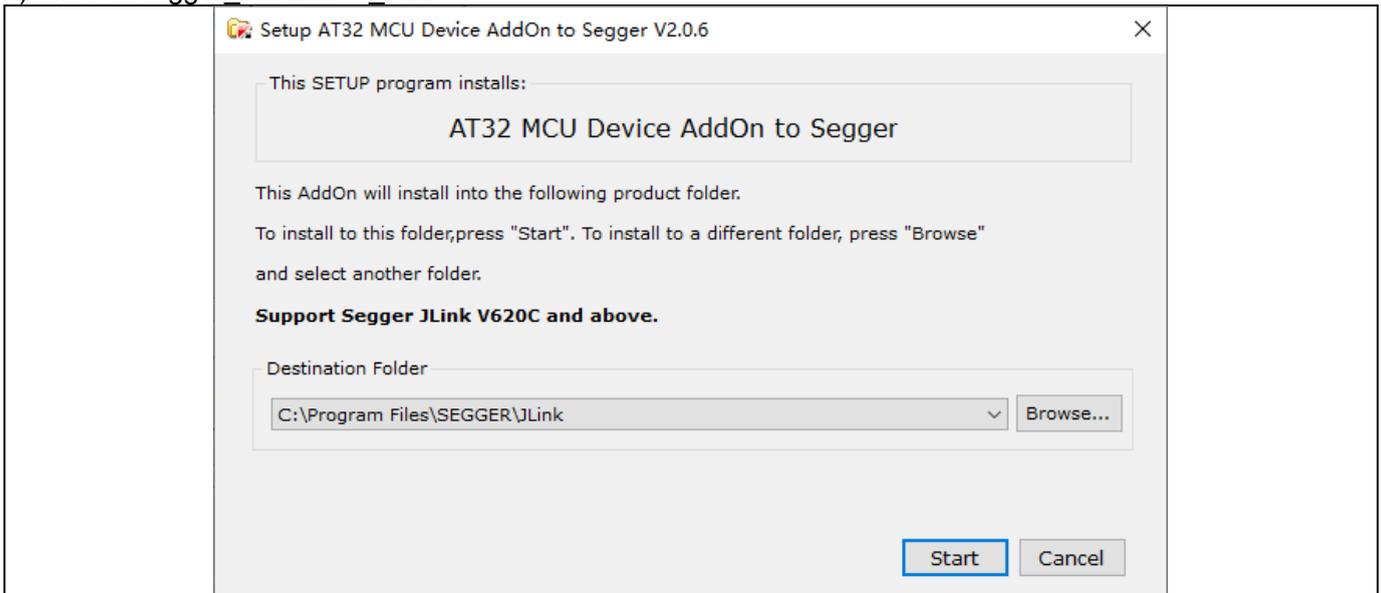


Method 2:

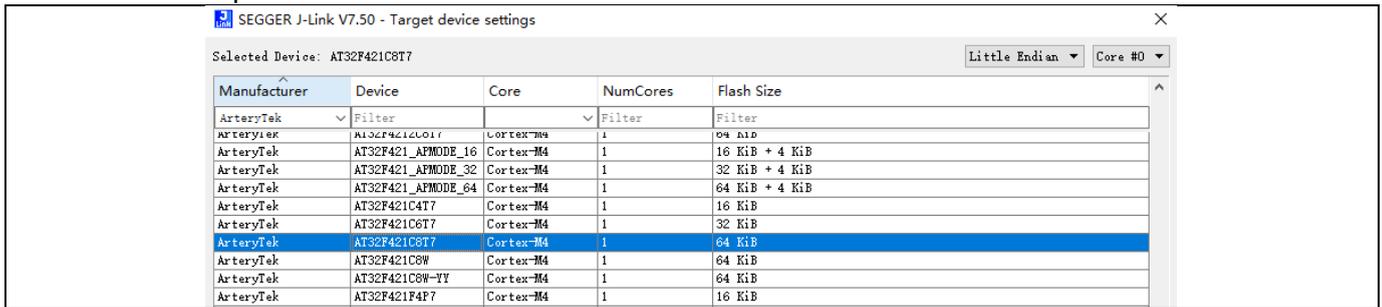
1) Install Segger software package (v6.20c and above), and tick the following options in the dialogue box (these options are ticked by default, do not change them)



2) Install Segger_AT32MCU_AddOn



3) When running Keil, you can select the corresponding MCU from the drop-down box. When running IAR, there is no drop-down box



Type: Development tool

Applicable products: All MCUs

Main function: None

Minor function: None

Document revision history

Date	Revision	Changes
2022.2.16	2.0.0	Initial release
2022.11.7	2.0.1	Added IAR descriptions. Updated Keil descriptions.

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